

INFORMATION DISCLOSURE

CITATION

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2380-152

09/712,123

APPLICANT

HORLIN et al

FILING DATE

GROUP

November 15, 2000

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U.S. PATENT DOCUMENTS

*EXAMINER

INITIALS

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IF APPROPRIATE

5,537,400

7-1996

Diaz et al.

RECEIVED

JUL 1 2 2001

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FOREIGN PATENT DOCUMENTS

TRANSLATION

DOCUMENT

DATE _____

COUNTRY

CLASS

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YES

NO

00/38380

6-2000

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

Mirfakhraei, "Wafer-Scale Integration as a Technology Choice for High Speed ATM Switching Systems", Proceedings of the Annual International Conference on Wafer Scale Integration (from 1996 Proceedings of the Int'l. Conference on Innovative Systems in Silicon), US, New York, IEEE, vol. CONF. 6, 1994, pp. 282-291

Doi et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture iwth Input and Cross-Point Buffers", Proceedings of the International Switching Symposium, DE, Berlin, VDE Verlag, vol. SYMP. 15, 23 April 1995, pp. 384-388

*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

Form PTO-FB-A820 (Also PTO-1449)